

REVIEW PAPER

Uncooled Infrared Microbolometer Arrays and their Characterisation Techniques

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ABSTRACT

Recently, a lot of attention has been paid to infrared imaging using uncooled microbolometer detectors, primarily because of their low cost, and wide military and civilian applications. This paper reviews the basic principles of these detectors and some of the important works done internationally in this field. This paper presents some of the research work on titanium microbolometer carried out at Solid State Physics Laboratory, Delhi.

Keywords: IR detector, bolometer, titanium, microbolometer, uncooled microbolometer detectors, infrared imaging

1. INTRODUCTION

Infrared (IR) detectors are broadly classified into photon detectors and thermal detectors¹⁻³. Photon detectors are fast and more sensitive compared to the thermal detectors. As a result, these find applications in expensive weapon platforms, in astronomical observation instruments or in special medical instruments, where the performance is the main concern. On the other hand, the emergence of uncooled detectors⁴⁻³¹ has opened new opportunities for IR detection for both military and commercial applications due to their small size, less power consumption, and are less expensive, making these the ideal choice for applications requiring high unit numbers with relatively lower performance. The key factor in developing a highly sensitive detector is to develop a thermometer material compatible with silicon technology to achieve high thermal isolation in the smallest possible area. The microbolometer technology, well adapted for thin film process, leads to higher performance at the expense of more complex read-out integrated circuit design.

The recent advances in microelectromechanical systems (MEMS) technology allow fabricating sensitive thermal bolometric detectors on thermally isolated hanging membranes. A bolometer employs a characteristic of thermally sensitive layer that changes its sheet resistance according to the change of the temperature (the larger the resistance change, the higher the temperature coefficient of resistance (TCR), so, higher of sensitivity). Many materials have been used for IR active layer of bolometer such as metals (*Au*, *Pt*, *Ti*, etc.) and semiconductors (*VOx*, amorphous silicon, etc). The main thrust is to develop a technology that provides ultra low-cost thermal IR imagers.

In this paper, basic working principal of thermal detection and a comprehensive review of the microbolometer IR detector array technology with its characterisation techniques

have been presented. It also presents the work done at SSPL laboratory for the development of 16 x 16 titanium metal-film microbolometer arrays and their characterisation results.

2. BASIC PRINCIPLE OF THERMAL DETECTION

A bolometer measures the changes in the heat input from the surroundings and converts this into a measurable quantity such as a voltage or current. A bolometer therefore typically consists of an absorber and a thermometer, resulting in the increase in temperature due to absorption of IR radiations that ultimately causes a change in resistance of bolometer elements. The resistance change information is electrically transferred to the read-out integrated circuit (ROIC) for further processing. To obtain high sensitivity,

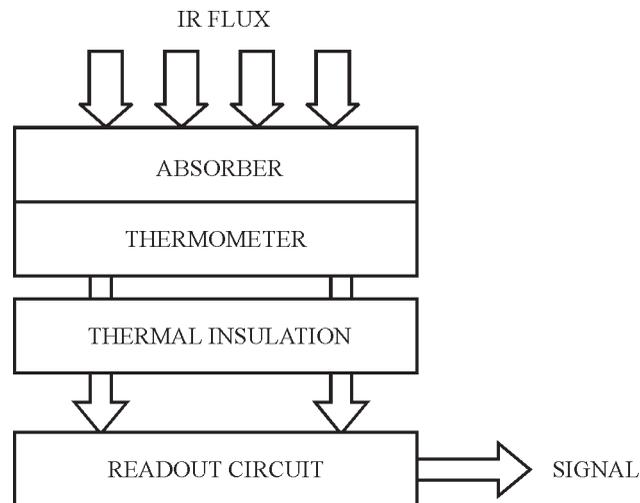


Figure 1. Schematic block diagram of a thermal detector (microbolometer).

the thermometer is kept thermally insulated with the ROIC substrate. The schematic block diagram of a typical microbolometer detector structure is illustrated in Fig. 1.

The analysis of thermal IR detectors begins by solving the heat flow equation that describes the temperature increase in terms of the incident radiant power. The heat flow equation describing the pixel is:

$$C \frac{d(\Delta T)}{dt} + G(\Delta T) = \eta P = \eta P_o \exp(j\omega t) \quad (1)$$

where, C is heat capacity of the sensitive area of a pixel; G is thermal conductance of the support structure; P_o is amplitude of modulated IR radiation power falling on pixel; η is absorbance of IR sensitive films; ω is angular frequency of modulation of the radiation; and ΔT is temperature increase of the sensitive area of the pixel.

This simplified equation assumes that the power dissipation in the sensitive area due to applied electrical bias can be neglected. The solution of Eqn (1) is:

$$\Delta T = \frac{\eta P_o \exp(j\omega t)}{G + j\omega C} = \frac{\eta P_o}{G(1 + \omega^2 \tau^2)^{1/2}} \quad (2)$$

where, τ is the thermal response time.

The temperature measurement is simplified when the resistance possesses a linear temperature dependence that holds true for most metals. For such a material, the resistance R can be expressed as

$$R = R_o \{1 + \alpha (T - T_o)\} \quad (3)$$

where, R_o is the resistance at the temperature T_o and α is the temperature coefficient of resistance (TCR). The temperature increase, $\Delta T = T - T_o$, due to the absorption of IR radiation is small enough so that the resistance change ΔR is linear with ΔT , i.e.,

$$\text{or } \Delta R \propto \Delta T \quad (4)$$

$$\Delta R = \alpha R \Delta T \quad (5)$$

where, $\alpha = (1/R) dR/dT$ (6)

The responsivity R_v of an IR pixel is defined as the output signal (voltage or current) divided by the input radiant power falling on the pixel. Let the output signal be the voltage V_s , then

$$V_s = I_b \Delta R = I_b \alpha R \Delta T \quad (7)$$

where, I_b is the bias current through the pixel. From Eqns (2) and (7), one gets

$$V_s = \frac{I_b \alpha R \eta P_o}{G(1 + \omega^2 \tau^2)^{1/2}} \quad (8)$$

The responsivity (R_v) is defined by the following relation.

$$R_v = V_s / P_o \quad (9)$$

Hence,

$$R_v = \frac{I_b \alpha R \eta}{G(1 + \omega^2 \tau^2)^{1/2}} \quad (10)$$

Equation (10) shows that the responsivity is directly proportional to the temperature coefficient of resistance (α) and inversely proportional to the thermal conductance

(G) associated with the principal heat loss mechanism. Both parameters are important for uncooled IR resistive bolometers. The value of G can range over several orders of magnitude, whereas the range of possible values of α is far less. The primary focus should be on the thermal isolation structure. The choice of resistive material is also an important parameter and should be a secondary consideration. It should be compatible to the processing of sensor. Equation (2) is basic to thermal IR arrays. It describes the temperature increase of the resistive area of the pixel when radiation of power amplitude P_o sinusoidally modulated with angular frequency ω falls on the sensitive area. The pixel temperature increases and decreases as the input radiant power rises and falls in an oscillatory manner. The transition between the low and high frequency regions is characterised by the thermal time constant t or thermal response time, that is defined as:

$$\tau = C/G \quad (11)$$

Another important parameter of thermal IR detector is detectivity and is given by

$$D^* = (R_v \sqrt{A \Delta f}) / V_n \quad (12)$$

where, Δf is the detector noise bandwidth (Hz), V_n is the total detector noise, and A is the detector area.

The important measure of the performance of IR imaging system is noise equivalent temperature difference (NETD) that is the difference in the temperatures of objects in a scene, which will produce a signal-to-noise ratio of 1. It is given by

$$NETD = \frac{4F^2 V_n}{\tau_o A R_v \left(\frac{\Delta P}{\Delta T} \right)_{\lambda_1 - \lambda_2}} \quad (13)$$

where, F is the focal ratio of the optics and τ_o is the transmittance of the optics, $(\Delta P / \Delta T)_{\lambda_1 - \lambda_2}$ is the change in power per unit area radiated by scene (or blackbody) at temperature T , wrt T measured within the spectral band from λ_1 to λ_2 . Smaller NETD indicates better performance.

3. DESIGN OF BOLOMETER

To obtain high performance, thermal arrays are designed in such a way so as to suppress the heat loss mechanisms. This is achieved by making the elements suspended using MEMS technology. The principal heat loss occurs only by the conduction through the support structure of suspended membrane to the substrate. The optimised support structure is the key to high performance thermal arrays. It provides three functions: mechanical support, a thermally conducting path, and an electrically conducting path. This structure can be fabricated by depositing thin films of selected materials like metals (titanium), vanadium oxide (VO_x) or any other thermally sensitive resistive material on silicon substrate that incorporates a ROIC's interface circuit at each pixel.

To understand the electrical and thermal behaviour of bolometer, let the heat capacity of bolometer be C , connected by a small thermal conductance G , to a heat sink held at a fixed temperature T_o , as shown in Fig.

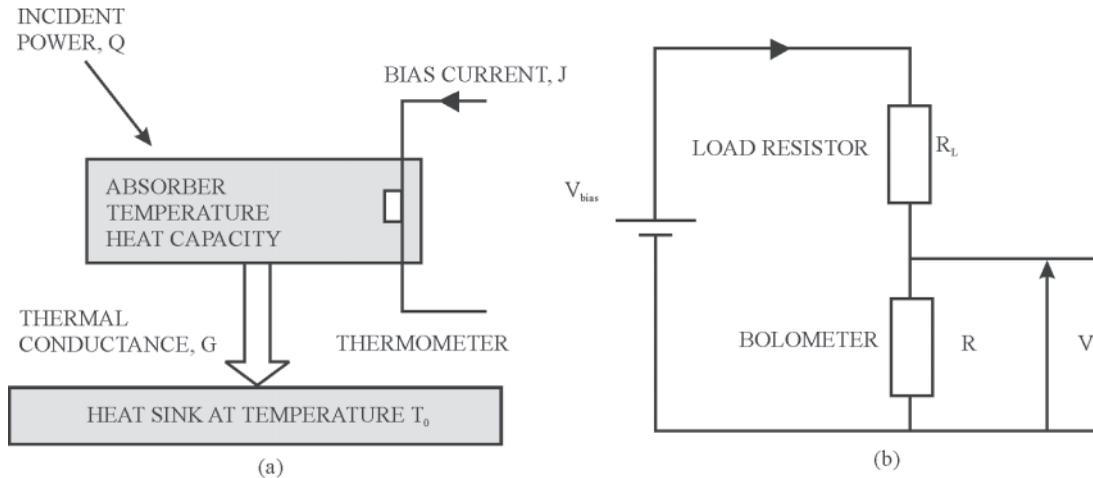


Figure 2. (a) Schematic illustration of bolometer operation and (b) bolometer bias circuit.

2(a). Figure 2(a) shows the schematic illustration of bolometer operation and Fig. 2 (b) shows its bias circuit. The energy E of the incident radiation is converted into heat in the absorber, leading to a temperature rise $\Delta T = T - T_0 = E/C$, until the radiation power flowing into the absorber is equal to the power flowing into the heat sink through the weak thermal link. The temperature rise is subsequently measured and is directly proportional to the deposited energy.

A constant current I , generated from the load resistor R_L and bias voltage V_{bias} , flows through the bolometer. Provided this bias power ($P_{bias} = V_{bias}I$) remains constant, the incoming signal power (P_{signal}), absorbed by the bolometer, will cause the bolometer temperature T to increase according to: $T = T_0 + (P_{signal} + P_{bias})/G$. This temperature rise causes a change in the resistance of the bolometer and consequently in the voltage across it. This change in voltage is amplified and measured. The thermometer is therefore made of a material that ideally exhibits a large change in resistivity for a small change in temperature.

The support structure is designed having excellent thermal isolation, that is, a very small value of G , expressed in $W/^\circ C$. Secondly, a detection mechanism and material are selected that, in combination with the support structure, provide high responsivity. Finally, the heat capacity C of the sensitive element expressed in $J/^\circ C$ is designed to be low enough to meet the response time (τ) requirement.

For example, for an array operating at the 30 Hz frame rate, the pixel response time should be shorter than $1/30$ s. Common practice is to make the pixel response time one-third of the reciprocal of the frame time, say 10 ms. A structure, to design with G say equal to $1 \times 10^{-7} W/^\circ C$, must have C equal to $1 \times 10^{-9} J/^\circ C$ to meet the response time requirement, i.e., $\tau = C/G = 10$ ms. The pixel area is defined by system requirement. Now, the adjustable parameter is only the element thickness. Assuming a $50 \mu m$ pixel attaining a value of $C = 1 \times 10^{-9} J/^\circ C$ will require a thickness of the sensitive layer not more than $1 \mu m$ for most of the materials. Thus, a membrane should be of the order of $1 \mu m$ thickness or less.

The thermal time constant, τ , as defined by Eqn (11) is the final performance parameter determining the spatial resolution of a thermal imaging system. The total thermal capacitance, C is given by

$$C = A (\rho_1 c_1 d_1 + \rho_2 c_2 d_2 + \rho_3 c_3 d_3 + \dots) \quad (14)$$

Here, ρ , c and d , the density, specific heat, and material thickness, respectively of the different material layers of the pixel element. For example, for the design that contains the stack of Si_3N_4 , Ti and TiN , one gets

$$C = V\rho C_{(Si_3N_4)} + V\rho C_{(Ti)} + V\rho C_{(TiN)} \quad (15)$$

where, V is the volume.

The total thermal conductance ($W/^\circ C$) given by

$$G = G_{leg} + G_{rad} + G_{gas} \quad (16)$$

where, G_{leg} is thermal conductance due to leg, i.e., support structure, G_{rad} is thermal conductance due to radiation and G_{gas} is the conductance due to gaseous conduction. The thermal conductance due to support structure, i.e., from leg (G_{leg}) is the dominant conductance in comparison of conductance through radiation and conductance through gas. Thermal conductance, G_{leg} , due to leg which connects the pixel element to silicon substrate to first approximation, is given by

$$G_{leg} = KA/L \quad (17)$$

where, K is the thermal conductivity, A is the area, and L is the length.

Equation (17) can be used in several conductance paths of different materials each component being added to obtain the total conductance for example of Ti -bolometer silicon nitride (Si_3N_4) membrane or silicon dioxide (SiO_2) membrane and passivation with the same material layers and metal (Ti) electrode layer. The G_{leg} for two legged structures is given as

$$G_{leg} = 2 KA/L \quad (18)$$

$$G = 2K_{Ti}A_{Ti}/L + 2K_{Si_3N_4}A_{Si_3N_4}/L \quad (19)$$

It is most important that the legs must constitute of material layers only having mechanical support and thermal IR detection electrode, which in the present case is the

T_i . Here, K_{Ti} is thermal conductivity of titanium layer on leg ($W/cm^{\circ}C$), $K_{Si_3N_4}$ is thermal conductivity of silicon nitride layer on leg ($W/cm^{\circ}C$), A_{Ti} is area of the cross sectional path of titanium layer on leg (cm^2), $A_{Si_3N_4}$ is area of the cross-sectional path of silicon nitride leg (cm^2), and L is length of leg (cm).

The response time τ , can be adjusted by selecting appropriate values of the dimension of the legs of the support structures down which heat flows to the substrate. Therefore, to design a bolometer for certain value of τ , time constant one has to compromise between leg length and membrane thickness.

4. INTERNATIONAL SCENARIO AND RECENT DEVELOPMENTS

Uncooled IR detectors are now available for various applications. Their simple operating conditions are similar to those of CMOS active pixel sensor (APS) or CCD digital camera. These have already shown their potential for many commercial and military applications. Nevertheless, as these are not cooled, so cold shield could be added to determine with precision the IR irradiance level. Consequently, these are very sensitive to temperature and environmental conditions and camera manufacturers have to take this behaviour into account to address thermographic applications by adding an internal temperature shield between the detector and the lenses. To fulfill this demand, a radiometric version of standard 320 x 240 detectors have been developed by M/S SOFRADIR and ULIS, France.

There is persistent worldwide effort to implement very large format microbolometer uncooled IR detector arrays at low cost⁵⁻²⁵. For this, many researchers are trying to develop monolithic uncooled IR detectors with read-out electronics on CMOS wafer itself. The compatibility of the detectors with CMOS technology, to achieve this integration, is one of the main concerns for the development of low-cost detectors. Implementation of microbolometers on surface micromachined, bridges on CMOS-processed wafers is one of the most common approaches for uncooled IR imaging⁵⁻¹².

The bolometers are fabricated on these thermally-isolated and suspended bridges, as shown in Fig. 3. To improve the sensitivity performance, there are efforts to implement microbolometers using various materials,

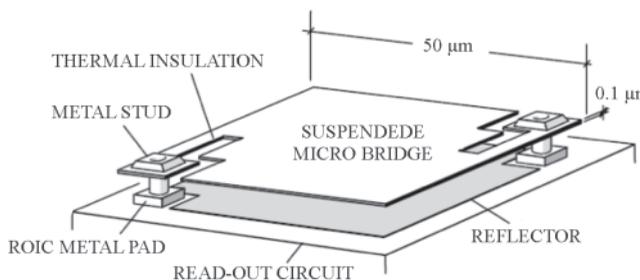


Figure 3. Schematic of the bolometer approach employing suspended microbridges.

such as vanadium oxide (VO_x)⁵⁻⁷, amorphous silicon (a-Si)⁸⁻¹⁰, polycrystalline silicon-germanium (poly SiGe)^{11,12}, yttrium barium copper oxide ($YBaCuO$)^{13,14}, and metal films^{15,26}. A high performance, very close to those of cooled infrared detectors, has been demonstrated with this approach, resulting in focal plane array (FPA) sizes as large as 640×480 and pixel sizes ranging from $50 \mu m$ down to $17 \mu m$, especially using VO_x and a-Si at much lower cost⁵⁻¹⁰.

Another approach for highly sensitive microbolometer is the use of silicon $p-n$ junction diodes as the temperature sensitive element instead of resistive microbolometers in the detector arrays²⁷⁻²⁹. These are based on suspended multiple series diodes fabricated on SOI CMOS wafers, and successful implementation of 320×240 FPAs with $40 \mu m$ pixel pitch and 640×480 FPAs with $25 \mu m$ pixel pitch have been reported²⁷⁻²⁹. This approach provides uniform arrays with very good potential for low-cost, high performance uncooled detectors. However, its fabrication is based on a dedicated in-house SOI CMOS process, where intra-CMOS MEMS process steps are included, limiting the cost reduction. The schematic of this approach is illustrated in Fig. 4.

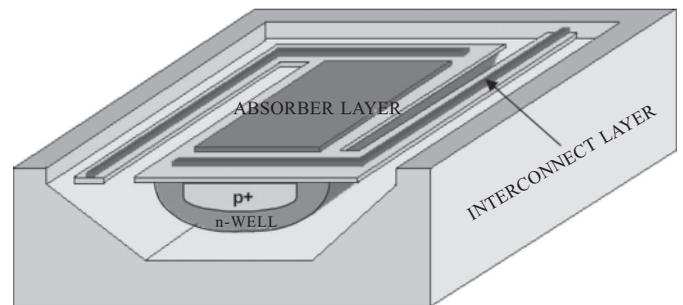


Figure 4. Si $p-n$ junction diode microbolometers architecture.

Recently, a different approach has been proposed for very low-cost microbolometer array development, where the detector material is obtained with very thin $Si/SiGe$ single crystal multilayers³⁰. Due to the high temperature epitaxial growth process, incompatible to CMOS read-out electronics, the material is grown on a separate substrate, and transferred on top of the CMOS read-out electronics using a 3-D integration process³⁰⁻³¹, as depicted in Fig. 5. This approach, though provides a low-cost solution, requires a very complicated process with unconventional production methods that limit the yield.

LETI and ULIS of France have chosen, from the beginning, to develop first, a monolithic microbolometer technology fully compatible with commercially available CMOS technology and secondly, amorphous silicon-based thermometer. This silicon approach has the greatest potential for reducing infrared detector manufacturing cost. After the development of the technology, the transfer to industrial facilities has been performed in a short period of time and the production is now ramping up with ULIS team in new facilities. LETI and ULIS are now working to facilitate the IR FPA integration into equipment to address a very large market⁹⁻¹⁰.

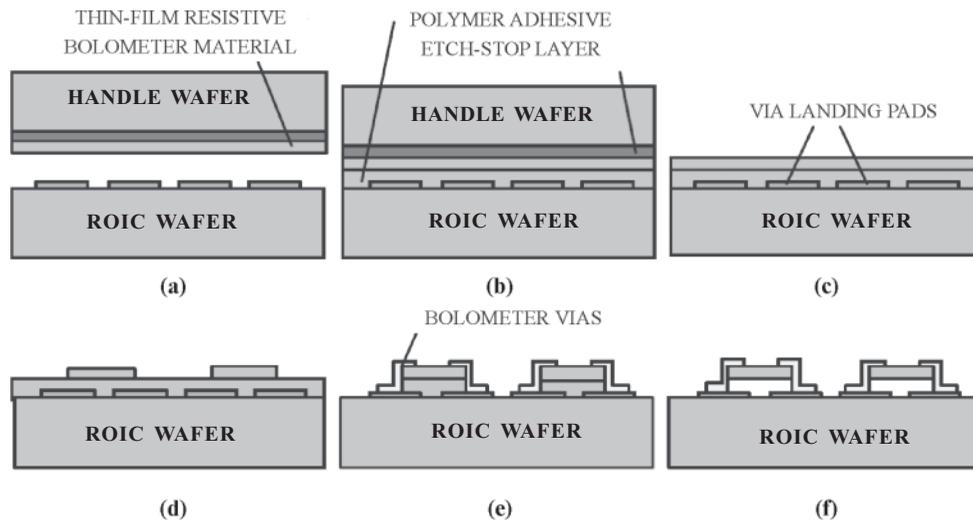
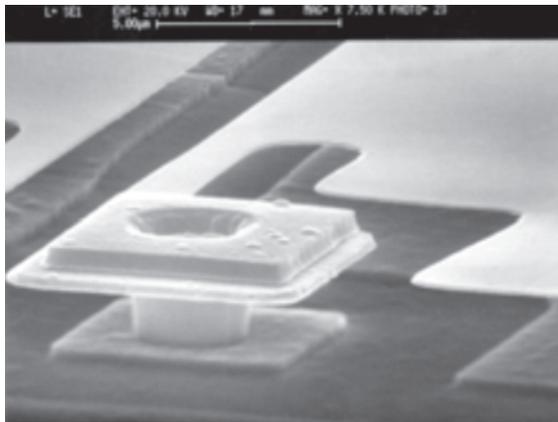


Figure 5. Schematic representation of 3-D integration of uncooled IR bolometer arrays: (a) Separate fabrication of ROIC and handle wafer containing bolometer material, (b) adhesive wafer bonding, (c) thinning of handle wafer, (d) bolometer definition (e) via formation, and (f) etching of sacrificial polymer adhesive layer.



(a)



(b)

Figure 6. Microbolometer IR detector arrays developed by ULIS: (a) SEM picture of bolometer element interconnection, and (b) packaged staring arrays.

Figure 6 shows the SEM picture and the packaging of the microbolometer IR detector array.

For ultra low-cost applications, the latest trend is the implementation of the detector arrays together with their read-out circuitry on a standard CMOS or SOI-CMOS processing steps, using some simple post-CMOS etching steps, where neither any critical lithography nor any detector material deposition steps are needed.

5. BOLOMETER ARRAYS DEVELOPED AT SOLID STATE PHYSICS LABORATORY

The Solid State Physics Laboratory is engaged in the development of microbolometer-based IR FPA. Currently, the microbolometer arrays in 16×16 and 32×32 formats are under development. These are made using *Ti* as a bolometer material with TCR of about 0.25 per cent. Figures 7(a) and 7(b) show the schematic of the bolometer element

and the connection scheme in 16×16 arrays without using ROIC. Figure 7(c) shows the SEM picture of the bolometer element. Cross-sectional view and the air gap of the element can be seen in the figure.

This scheme reduces the number of pads required for accessing all bolometers. In this configuration, only³² pads are required while in the case of individual access, one needs 512 (2×256) pads. Also, it is very close to the actual array from the processing point of view. Due to this complex switching structure, all other bolometers form a network in parallel with the bolometer being accessed. This not only affects the apparent resistance of the element being accessed, but also causes the signal of an individual element to diffuse over the whole array. To get rid of this unwanted networking effect, we have developed a method for extracting elements from this complex network has been developed³³.

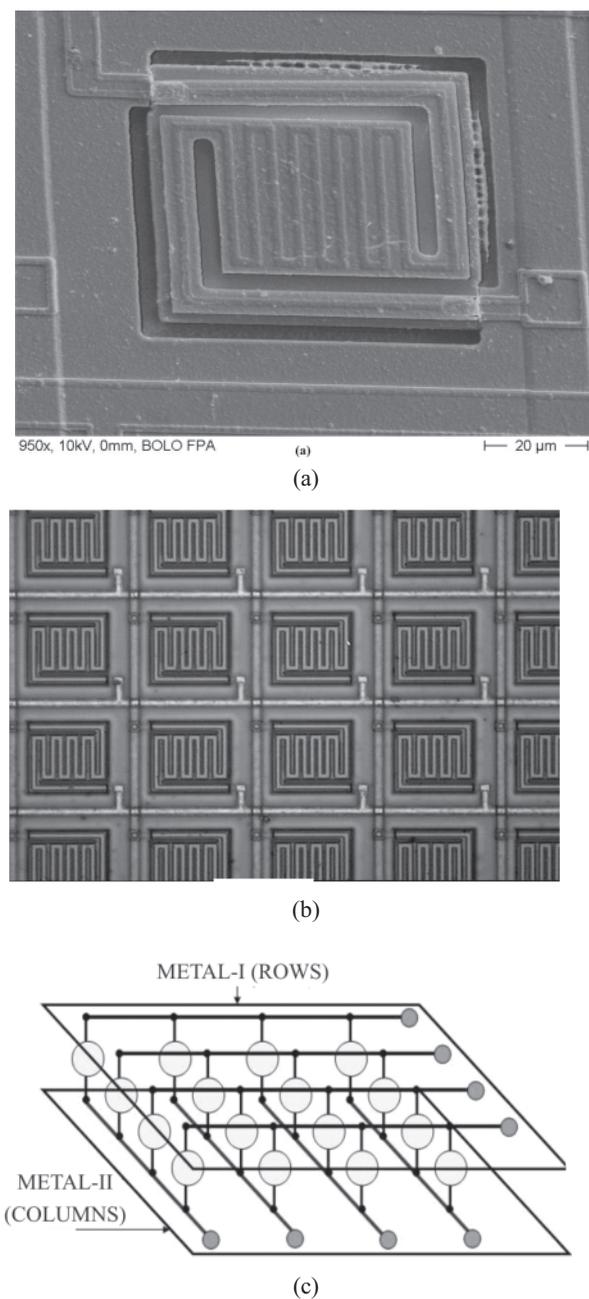


Figure 7. 16x16 Ti-microbolometer arrays developed at SSPL: (a) SEM picture of a bolometer element, (b) photograph of a part of the array, and (c) connection scheme.

6. CHARACTERISATION TECHNIQUES

In this section, the various measurements and their techniques generally used for characterising these arrays, as reported in literature^{11,16,32-34} are described. The measurements are generally performed on each individual element of the array to extract the performance parameters of the detector. For extracting the electrical and thermal parameters, like TCR, G and C , current-voltage characteristic behaviour has been analysed and for the extraction of responsivity and sensitivity parameters, radiometric measurements were performed. The electrical output, as a function of chopping frequency, was used to extract the thermal time constant

and IR absorbance. The measurement setups for electrical and radiometric measurements are shown in Figs. 8(a) and 8(b).

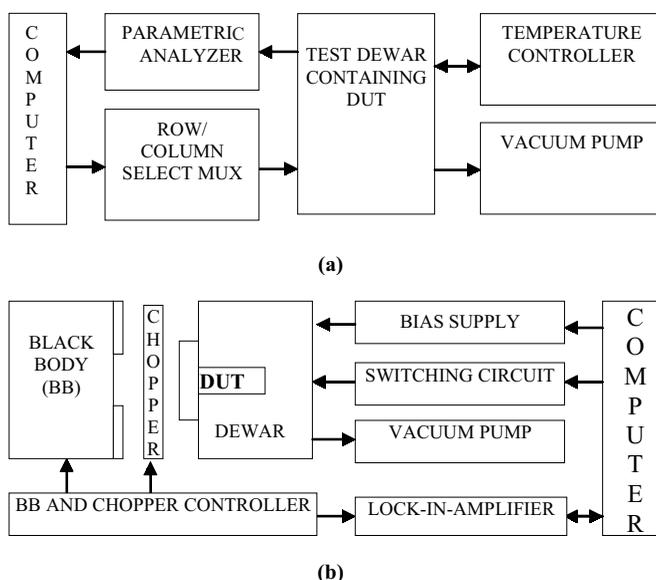


Figure 8. Block diagram of: (a) electrical measurement setup and (b) radiometric measurement setup for microbolometer arrays.

Various measurements and the corresponding parameters are listed in Table 1. The details of these measurements and parameter extraction are discussed. Other than these conventional techniques, many advanced techniques have been reported in literature. These techniques employ ac electrical technique for parameter extraction, single electrical measurements, and non-optical methods for complete characterisation of bolometer IR detectors.

The IV characteristics of the bolometer array are obtained using the setup shown in Fig. 8(a) at a stabilised temperature (normally 300 K, i.e., the room temperature). These characteristics

Table 1. List of measurements and associated parameters

Measurement	Parameters	Information extracted
Electrical measurements	Resistance mapping	1. Offset non-uniformity
		2. Bad elements
	Current-voltage characteristics	3. Thermal conductance (G)
		4. Thermal breakdown point
		5. Temperature coefficient of resistance (TCR)
Radiometric measurements	Signal and noise measurements	6. Specific detectivity (D^*)
		7. Responsivity (R_v)
	Frequency response of signal	8. Thermal time constant (τ)
		9. Thermal absorbance (η)

deviate from ohmic behaviour due to self-heating effects. The dissipated electrical energy $I^2 R t$ is converted to heat. A part of this heat is conducted through the supporting legs. A very small part of this heat is radiated into the surrounding atmosphere, and the remaining heat increases the bolometer temperature, and hence its resistance. This effect is known as self-heating. This increases the resistance according to the relation described in Section 2.

Assuming that heat conduction through support legs is the only heat loss mechanism, following relation is obtained.

$$I^2 R = G(T - T_0) \tag{20}$$

Eliminating $(T - T_0)$ in Eqns (3) and (20), one obtains

$$\frac{1}{R} = \frac{1}{R_0} - \frac{\alpha}{G} I^2 \tag{21}$$

This implies that $1/R$ changes linearly with the I^2 and if $1/R$ vs I^2 is plotted, the slope of the line will contain the information of α/G ratio. This is used to extract the thermal conductance G of the bolometer. For small values of current, the self-heating, being very less, is not clearly visible in the characteristics. Thus, relatively high currents need be applied, but that may cause excessive heating and permanent damage. Therefore, this measurement requires the knowledge of maximum tolerable current by the bolometers. Using the current-voltage-temperature ($I-V-T$) data, the change in resistance as a function of temperature can be plotted and the TCR can be extracted using Eqn (6). By knowing the values of TCR and G , one may easily find C using Eqn (11).

For evaluation of IR sensing capability, the signal of the bolometer elements is measured for a calibrated IR radiation falling onto it, noise of those elements and frequency response. A source of IR radiation (blackbody) is used to generate this IR radiation, which is mechanically chopped, as shown in the setup in Fig. 8(b). The device under test (DUT) finds the radiation intensity changing with the chopping frequency. The detector elements are biased with a constant current source. The incident IR signal causes change in the detector resistance, which is converted into the voltage change by the bias current and monitored by a lock-in-amplifier locked with the chopper frequency. This measurement provides various system-related performance parameters like D^* , R_v , τ and η . The specific detectivity is calculated using the following relation:

$$D^* = \frac{\text{SNR}}{\text{Power}} \sqrt{\text{Area} \cdot \Delta f} \tag{22}$$

The electrical signal output for the given input power is decided by the detector's responsivity, which is calculated using following the relation:

$$R_v = \text{output electrical signal} / \text{input radiation power} \tag{23}$$

where, R_v is the voltage responsivity, having units of volt per watt. Thermal time constant decides how fast the bolometer may respond, and that further sets the limit on frame rate

of the imager. Therefore, if one keeps on increasing the chopping frequency, the bolometer will get less and less time to respond to the radiation falling on it, causing less signal output. If the signal (or responsivity) wrt the chopping frequency is plotted, one gets the frequency response. This curve will therefore contain information of bolometer's response time, i.e., time constant. One may re-state the bolometer's responsivity Eqn (10) by the following relation:

$$R_v = \frac{\alpha \eta R I_b}{G \sqrt{1 + \omega^2 \tau^2}}$$

For extracting the τ , the frequency response was normalised by its very low frequency value, R_{v0} . The normalised responsivity (also called relative responsivity) is given by the Eqn (24) and may be used to find the value of τ .

$$R_{v(\text{Normalised})} = \frac{1}{\sqrt{1 + \omega^2 \tau^2}} \tag{24}$$

After determining the value of τ , one may also find, as all other parameters in Eqn (24) are now known. SSPL has performed the characterisation of a number of our bolometer arrays developed. Figure 9 shows the characterisation results and Table 2 summarises them. The details of measurement and result are discussed³⁴.

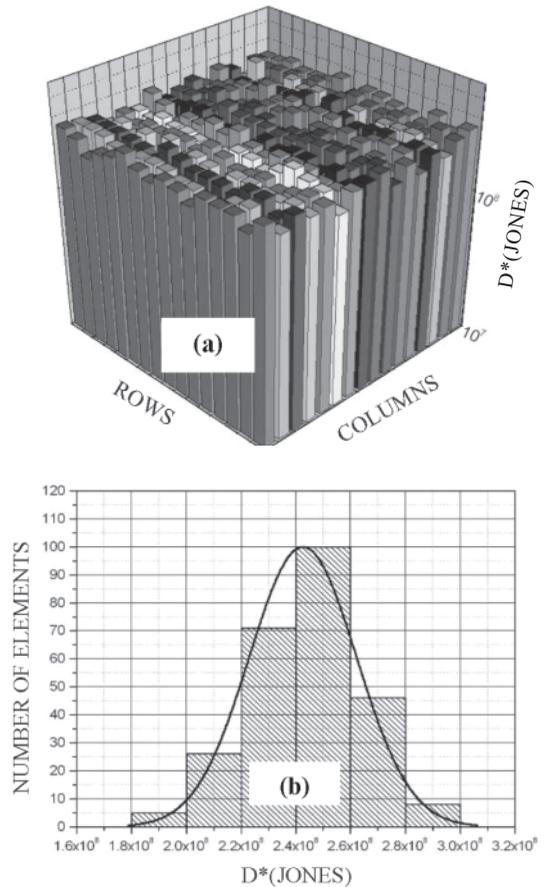


Figure 9. Characterisation results of a typical 16x16 Ti-microbolometer array developed by SSPL: (a) D^* bar chart, and (b) D^* histogram.

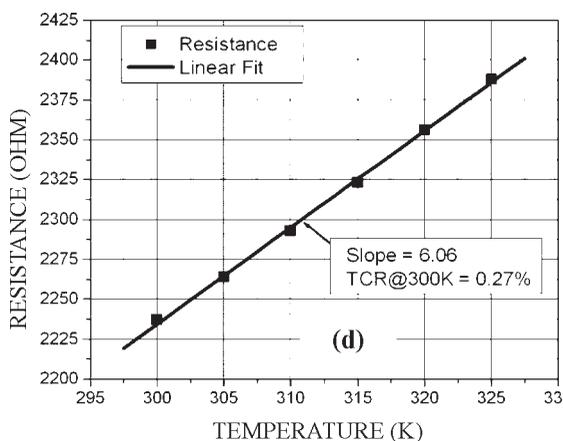
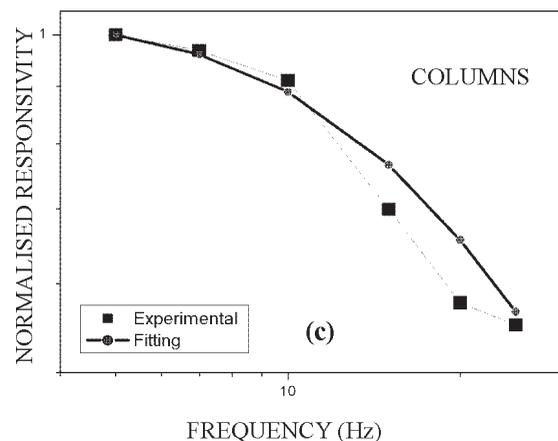


Figure 9. Characterisation results of a typical 16x16 *Ti*-microbolometer array developed by SSPL: (c) normalised frequency response, and (d) *R-T* curve to determine the TCR.

Table 2. Measured values of various parameters

Parameter	Measured value	
	Average	Variation
TCR (%)	0.27	0.24-0.30
Thermal conductance ($\mu\text{W/K}$)	0.162	0.160-0.172
Thermal time constant(ms)	9.91	9.72-10.60
Detectivity ($\times 10^8 \text{ cm Hz}^{1/2}/\text{W}$)	1.44	0.77-3.19

Efforts are on to demonstrate the basic imaging in the laboratory conditions using 16 x 16 and 32 x 32 (under development) arrays with the help of external hardware in absence of a proper ROIC. The concept of the hardware used is discussed³⁵.

7. CONCLUSIONS

Some important fundamentals and theory of microbolometer infrared detectors along with some international developments have been reviewed. The methods of characterising the microbolometer arrays. The characterisation of 16x16 *Ti*-microbolometer IR detectors without using

ROICs has been presented. The method may be used for initial development and evaluation of detector process technology leading to the fabrication of large 2-D bolometer IR detector arrays. Finally, some results of microbolometers developed by SSPL have been presented.

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