

# Performance Analysis of GaN/AlGaN HEMTs Passivation using Inductively Coupled Plasma Chemical Vapour Deposition and Plasma Enhanced Chemical Vapour Deposition Techniques

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## ABSTRACT

In the present paper SiN thin film has been studied as a passivation layer and its effect on AlGaN/GaN HEMTs is investigated using two different deposition techniques i.e PECVD and ICPCVD. AlGaN/GaN HEMTs devices passivated with optimised SiN film have delivered lower gate leakage current (from  $\mu\text{A}$  to  $\text{nA}$ ). Device source drain saturation current ( $I_{ds}$ ) increased from  $400\text{mA/mm}$  to  $\sim 550\text{ A/mm}$  and the peak extrinsic trans-conductance increased from  $100\text{ mS/mm}$  to  $170\text{ mS/mm}$  for a  $0.8\ \mu\text{m}$  HEMT device. The optimised SiN passivation process has resulted in reduced current collapse and increased breakdown voltage for HEMT devices.

**Keywords:** GaN; Passivation; SiN; Plasma enhanced chemical vapour deposition; PECVD; Inductively coupled plasma chemical vapor deposition; ICPCVD

## 1. INTRODUCTION

GaN based high electron mobility transistors (HEMTs) on SiC substrate have shown potential electrical characteristics that make them suitable candidate for high frequency, high power and high temperature applications. These devices are of tremendous interest in high power applications at microwave frequencies. These devices are known for better IV characteristics and record high output power at RF frequencies. However, GaN HEMTs suffer from current collapse and frequency dispersion mainly due to trapping of electron in the active channel region<sup>1-18</sup>. The trapping effect is mainly due to defects, dislocations which might have incorporated during HEMT structure growth. A lot of work has been carried out to investigate and understand the effect of frequency dependent behaviour and reduction in drain current which degrades the device performance. Passivation by suitable dielectrics generally leads to mitigate the current collapse and increases off state break down voltage. People have presented work on SiN passivation layer for AlGaN/GaN HEMTs which has shown an increase in output power and breakdown voltage<sup>4,8,9</sup>.

In the present paper we have studied the SiN passivation of GaN HEMTs carried out by two different techniques i.e. plasma enhanced chemical vapour deposition (PECVD) and inductively coupled plasma chemical vapour deposition (ICPCVD). A comparative study of passivated and unpassivated AlGaN/GaN HEMTs with a gate-length of  $0.8\ \mu\text{m}$  has been presented for both deposition techniques as shown in Fig. 1. The effect

of silicon nitride passivation on device DC characterisation has been compared. We have observed significant increase in saturation current and breakdown voltage for both the silicon nitride deposition process.

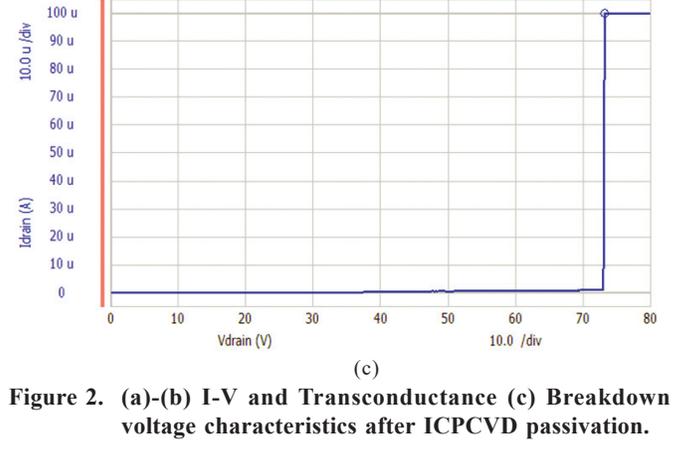
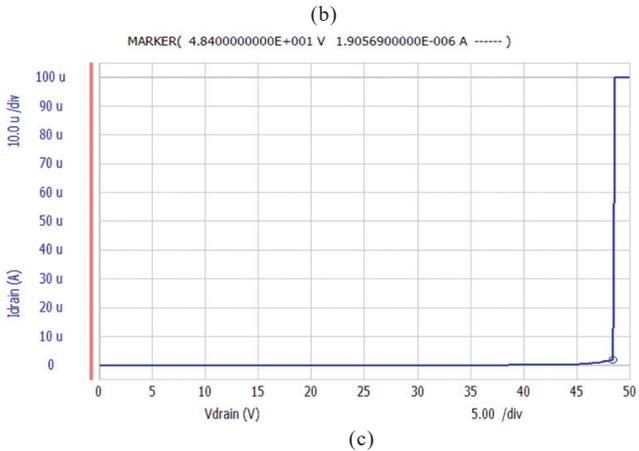
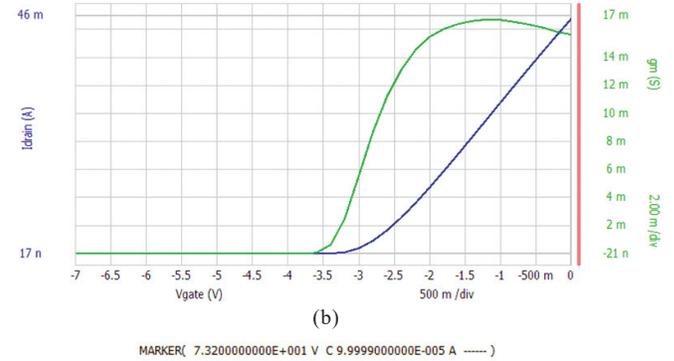
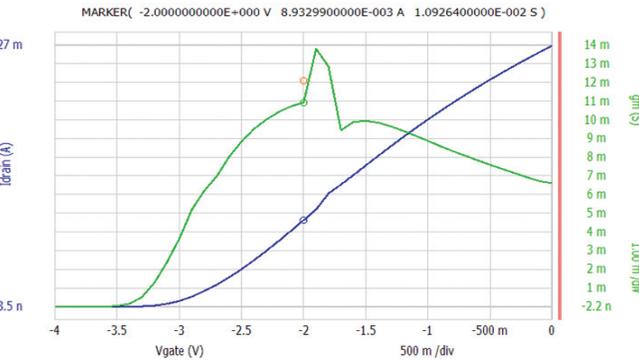
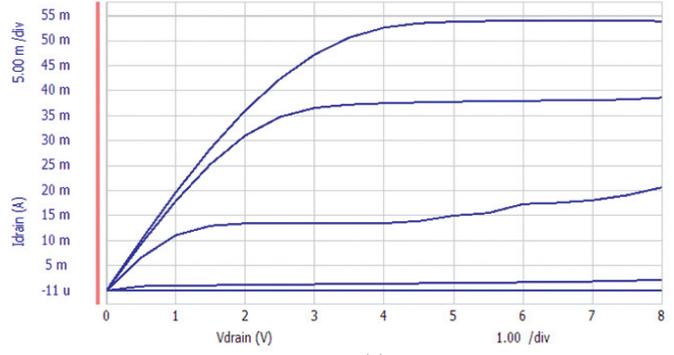
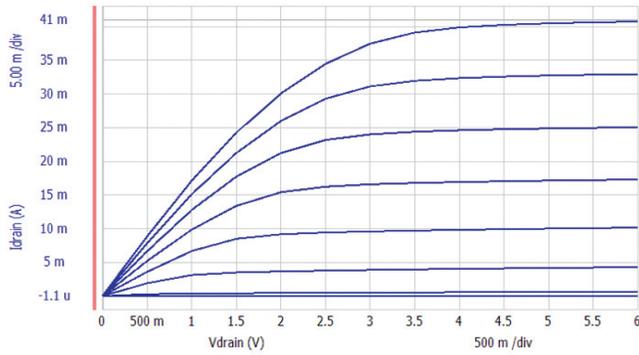
## 2. EXPERIMENTAL WORK

The study is carried out on AlGaN/GaN HEMTs device fabricated on metal organic chemical vapour deposition (MOCVD) on silicon carbide (SiC) substrate. The HEMT structure consists of undoped GaN buffer and  $25\text{ nm} - 27\text{ nm}$  AlGaN. Hall measurements showed a sheet carrier concentration of  $1 \times 10^{13}\text{ cm}^{-2}$  and an electron mobility of  $1600\text{ cm}^2/\text{Vs}$  at room temperature. The device isolation is achieved by mesa etching using  $\text{BCl}_3/\text{Cl}_2$  plasma etching in inductively coupled plasma reactive ion etching (ICPRIE) system.

After source, drain and gate formation the devices have been processed for passivation, One half of the wafer is passivated by SiN film deposited by PECVD method and other half of the wafer is passivated using SiN film deposited by ICPCVD technique. This methodology is adopted to study the effect of both passivation techniques on same HEMT structure and device geometry before and after passivation<sup>16-18</sup>.

In PECVD<sup>11-13</sup> method we have used silane ( $\text{SiH}_4$ ), ammonia ( $\text{NH}_3$ ) and nitrogen ( $\text{N}_2$ ) gases for deposition of silicon nitride film. The process parameters used for PECVD method are, RF power  $250\text{ W}$ , temperature  $300\text{ }^\circ\text{C}$  while in ICPCVD process silane ( $\text{SiH}_4$ ) and nitrogen ( $\text{N}_2$ ) gases are used at  $100\text{ }^\circ\text{C}$  temperature for silicon nitride deposition.

We have kept the silicon nitride film thickness  $\sim 1000\text{ \AA}$  in both the cases to avoid any variation in 2DEG due to



**Figure 1. (a)-(b) I-V and Transconductance (c) Breakdown voltage characteristics before passivation.**

SiN thickness. To evaluate device performance devices are characterised before and after the passivation using parametric analyser B1500A for IV characteristics.

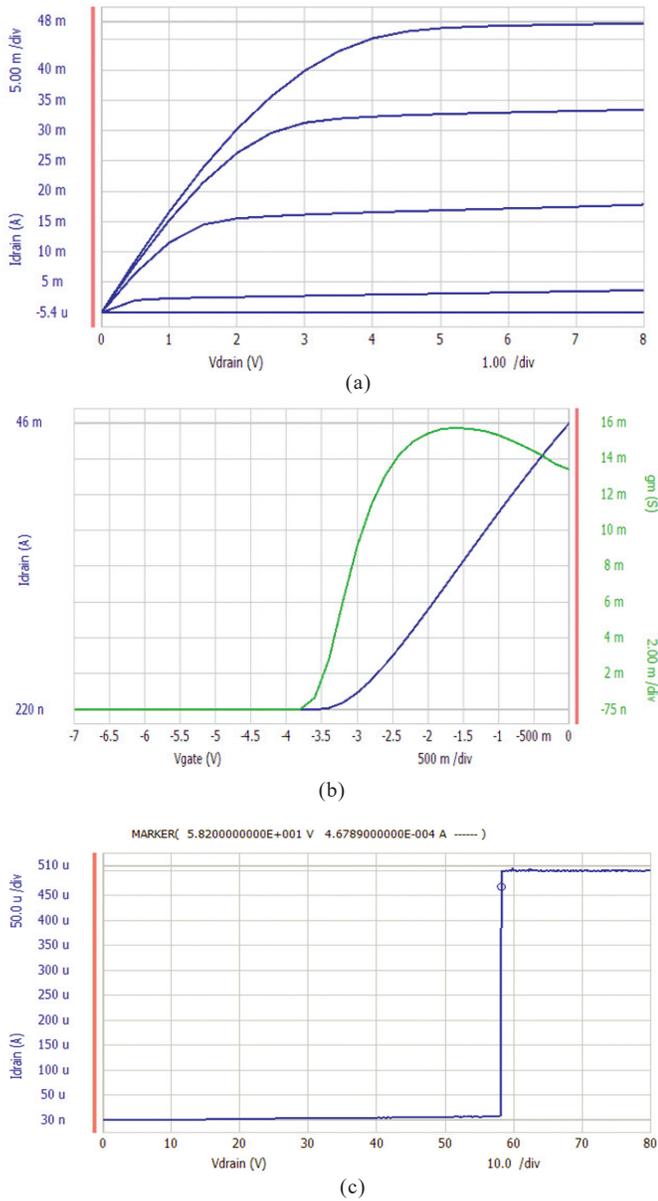
### 3. RESULTS AND DISCUSSIONS

We have observed increase in source drain saturation current in both type of passivation techniques. In PECVD passivation the HEMT devices has shown increase in drain current from 400 mA/mm to 480 mA/mm as shown in Figs. 2(a) and 2(b), whereas in ICPCVD passivation the current has increased to 550 mA/mm as shown in Figs. 3(a) and 3(b). Breakdown voltage measured is 50 V for PECVD passivation as shown in Fig. 2(c). In case of ICPCVD passivation the breakdown voltage is increased to 75 V on the same device structure as shown in Fig. 3(c). The passivated HEMT devices by ICPCVD method have shown significant increase in performance compare to PECVD passivation.

**Figure 2. (a)-(b) I-V and Transconductance (c) Breakdown voltage characteristics after ICPCVD passivation.**

Drain current collapse occurs during high frequency operation in GaN devices. This is due to trapping effects caused by surface states and considered to be responsible to this depression in current. This is mainly caused by existence of a ‘virtual gate’, which might have depleted the transistor access regions. The virtual gate is formed between the gate and drain electrodes. Traps in either the GaN buffer or AlGaN barrier layers also lead to the poor performance of device as they act as source for leakage current. Surface passivation by a suitable dielectric film is essential to mitigate the current collapse effect<sup>1,2,6,16</sup>. Number of people have suggested that in SiN passivation Si incorporation at the AlGaN surface as shallow donor and helps in reducing surface traps and hence the current collapse.

We have observed a much higher increase in breakdown voltage in ICPCVD process in comparison to PECVD process. This may be due to the quality of silicon nitride. The PECVD silicon nitride film is less dense as compared to ICPCVD silicon nitride as the etch rate is higher for the PECVD deposited

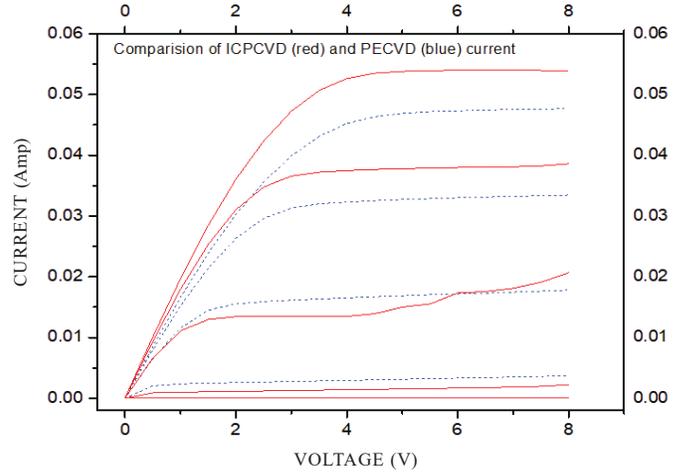


**Figure 3. (a)-(b) I-V and Transconductance, (c) breakdown voltage characteristics after PECVD passivation.**

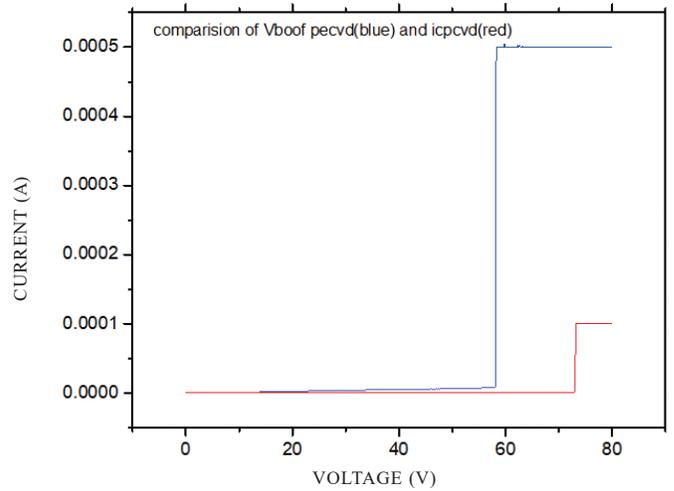
silicon nitride films in comparison to ICPCVD deposited nitride film. FTIR also has shown more hydrogen contents in PECVD silicon nitride than ICPCVD silicon nitride films. A comparison of source drain DC current for both ICPCVD and PECVD passivation process on HEMTs device performance is as shown in Fig. 4.

The increase in breakdown voltage can be attributed to the distribution of high field region through silicon nitride film used for passivation. Silicon nitride surface passivation film leads to suppression of surface states probably, which are trapping the electrons coming from gate. A comparison for breakdown voltage has shown in Fig. 5<sup>9,13</sup>.

Both type of passivation have resulted in improvement of the device performance. The channel conductivity was increased, and consequently the drain current and transconductance were also increased. The improvement observed with the passivation layer mainly due to induced variable stress from compressive



**Figure 4. Comparison of I-V characteristics of HEMT devices after ICPCVD and PECVD passivation.**



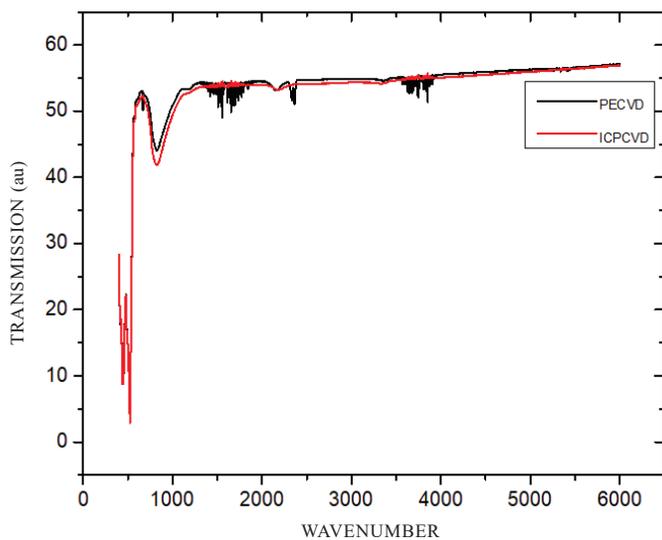
**Figure 5. Comparison breakdown voltage ( $V_{boff}$ ) of HEMT device after ICPCVD and PECVD passivation.**

PECVD nitride ( $-125$  MPa) to tensile ICPCVD nitride ( $\sim 50$ MPa)<sup>12-14</sup>. It is clearly evident that the devices with tensile passivation-induced stress exhibited the better performance.

The FTIR spectra for silicon nitride films for both PECVD and ICPCVD processes is as shown in Fig. 6. The FTIR spectrum is showing more hydrogen percentage (more N-H and Si-H bond compare to Si-N) in PECVD than ICPCVD grown films clearly indicating the better quality of ICPCVD films. The refractive index measurement and etch rate also confirming the same.

#### 4. CONCLUSIONS

The effect of SiN thin film passivation on AlGaIn/GaN HEMT device is analysed using two different techniques namely PECVD and ICPCVD. Carrier (electron) trapping at the SiN/GaN interface is considered to be mainly responsible for the behavior observed. Analysis of the device IV characteristics under pulsed regime with variable pulse width may reveal more information. SiN passivated HEMT devices with a gate-length of  $\sim 0.8 \mu\text{m}$  have shown better electrical (DC) characteristics with lower gate leakage current. Also the device breakdown



**Figure 6. FTIR spectra for PECVD and ICPCVD grown silicon nitride showing Si-N, Si-H and N-H absorption peaks.**

voltage increased significantly after passivation in comparison to un-passivated devices. For our HEMT devices, ICPCVD silicon nitride passivation exhibits better performance than PECVD passivation method, though both the techniques are suitable for passivation of GaN based HEMTs devices.

## REFERENCES

1. Vetry, R; Zhang, N Q ; Keller, S. & Mishra, U K. The impact of surface states on the DC and RF characteristics of AlGaIn/GaN HFETs. *IEEE Trans. Electron Devices*, 2001, **48**(3), 560-66.  
doi: 10.1109/16.906451
2. Binari, S C; Klein, P B & Kazior, T E. Trapping effects in GaN and SiC microwave FETs. *Proc. IEEE*, 2002, **90**(6), 1048-58.  
doi: 10.1109/JPROC.2002.1021569
3. Green, B M; Chu, K K; Chumbes, E M; Smart, J A; Shealy, J R & Eastman, L F. The Effect of Surface Passivation on the Microwave Characteristics of undoped AlGaIn/GaN HEMTs. *IEEE Electron Device Lett.*, 2000, **21**(6), 268-70.  
doi: 10.1109/55.843146
4. Kao, C J; Chen, M C; Tun, C J; Chi, G C; Sheu, J K; Lai, W C; Lee, M L; Ren, F & Pearton, S J. Comparison of low-temperature GaN, SiO<sub>2</sub>, and SiN<sub>x</sub> as gate insulators on AlGaIn/GaN heterostructure field-effect transistors. *J. Appl. Phys.*, 2005, **98**(6), 064506 -5.  
doi: 10.1063/1.2058173
5. Kordoš, P; Heidelberger, G; Bernat, J; Fox, A; Marso, M & Luth, H. High-power SiO<sub>2</sub>/AlGaIn/GaN metal-oxide-semiconductor heterostructure field-effect transistors. *Appl. Phys. Lett.*, 2005, **87**(14), 143501.  
doi: 10.1063/1.2058206
6. Bernat, J; Javorka, P; Marso, M & Kordoš, P. Conductivity and Hall effect measurements on intentionally undoped and doped AlGaIn/GaN heterostructures before and after passivation. *Appl. Phys. Lett.*, 2003, **83**(26), 5455-57.  
doi: 10.1063/1.1637154
7. Mittereder, J.A.; Binari, S.C.; Klein, P.B.; Roussos, J.A.; Katzer, D.S.; Storm, D.F.; Koleske, D.D.; Wickenden, A.E. & Henry, R.L. Current collapse induced in AlGaIn/GaN high-electron-mobility transistors by bias stress. *Appl. Phys. Lett.*, 2003, **83**(8), 1650-52.  
doi:10.1063/1.1604472
8. Gatabi, Iman Rezanezhad; Johnson, Derek W; Woo, Jung Hwan; Anderson, Jonathan W; Cona, Mary R; Piner, Edwin L & Harris, Harlan Rusty. PECVD Silicon Nitride Passivation of AlGaIn/GaN Heterostructures. *IEEE Trans. Electron. Devices.*, 2013, **60**(3), 1082-87.  
doi: 10.1109/TED.2013.2242075
9. Ohna, Yutaka; Nakao, Takeshi; Kishimoto, Shigeru; Maezawa, Koichi & Mizutani, Takashi. Effect of surface Passivation on breakdown of AlGaIn/GaN high-electron-mobility transistors. *Appl. Phys. Lett.*, 2004, **84**(12), 2184-86.  
doi: 10.1063/1.1687983
10. Feng, Z.H.; Zhou, Y.G.; Cai, S.J. & Lau, Kei May. Enhanced thermal stability of the two-dimensional electron gas in GaN/AlGaIn/GaN heterostructures by Si<sub>3</sub>N<sub>4</sub> surface-passivation-induced strain solidification. *Appl. Phys. Lett.*, 2004, **85**(22), 5248-50.  
doi: 10.1063/1.1828231
11. Tan, W.S.; Houston, P.A.; Hill, G.; Airey, R.J. & Parbrook P.J. Influence of dual-frequency plasma-enhanced chemical-vapour deposition Si<sub>3</sub>N<sub>4</sub> passivation on the electrical characteristics of AlGaIn/GaN Heterostructure field-effect transistors. *J. Electron. Mater.*, 2004, **33**(5), 400 - 407.  
doi: 10.1007/s11664-004-0191-x
12. Gregušova, D.; Bernat, J.; Drzik, M.; Marso, M.; Uherek, F; Novak, J. & Kordoš, P. Influence of passivation induced stress on the performance of AlGaIn/GaN HEMTs. *Phys. Status Solidi.*, 2005, **2**(7) 2619-22.  
doi: 10.1002/pssc.200461350
13. Jeon, Chang Min & Lee, Jong-Lam, Effects of tensile stress induced by silicon nitride passivation on electrical characteristics of AlGaIn/GaN heterostructure field-effect transistors. *Appl. Phys. Lett.*, 2005, **86**(17), 172101.  
doi: 10.1063/1.1906328
14. Hu, S M. Stress-related Problems in Silicon Technology. *J. Appl. Phys.*, 1991, **70**(6) R53-R80 .  
doi: 10.1063/1.349282
15. Bernat, J.; Wolter, M.; Javorka, P.; Fox, A.; Marso, M. & Kordoš, P. Performance of unpassivated AlGaIn/GaN/SiC HEMTs after short-term electrical bias stress. *Solid-State Electron.*, 2004, **48**(10-11), 1825-28.  
doi: 10.1016/j.sse.2004.05.020
16. Kordoš, P; Bernat, J; Marso, M; Luth, H; Rampazzo, F; Tamiazzo, G; Pierobon, R. & Meneghesso, G. Influence of gate-leakage current on drain current collapse of unpassivated GaN/AlGaIn/GaN high electron mobility transistors. *Appl. Phys. Lett.*, 2005, **86**, 253511-3.  
doi: 10.1063/1.1953873
17. Hashizume, Tamotsu; Ootomo, Ssinya; Oyama Susuma;

Konishi M. & Hasegawa, H. Chemistry and electrical properties of surfaces of GaN and GaN/AlGaIn heterostructures. *J. Vac. Sci. Technol. B*, 2001, **19**(4), 1675-81.

doi: 10.1116/1.1383078

18. Sahoo, D.K.; Lal, R.K.; Kim, H.; Tilak, V. & Eastman, L.F. High-field effects in silicon nitride passivated GaN MODFETs. *IEEE Trans. Electron Devices*, 2003, **50**(5) 1163-70.

doi: 10.1109/TED.2003.813221

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In the present study, he is instrumental in PECVD-ICPCVD SiN passivation, characterisation and dry etching processes for device fabrication.

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